ILD Large Prototype TPC tests with Micromegas

D. Attié, A. Bellerive, P. Colas, E. Delagnes, <u>M. Dixit</u>, I. Giamatoris, A. Giganon J.-P. Martin, M. Riallot, F. Senée, N. Shiell, Y-H Shin, S. Turnbull, R. Yonamine, M. Vujicic (Saclay, Carleton, Montreal, TRIUMF, KEK)



GEM & Micromegas options for ILC TPC

Micromegas panels for Large Prototype

Studies on resistive coatings

Installation at DESY

Software and simulation

Status of beam tests



lrfu









saclay

ILC goal - to measure Higgs with precision limited only by the knowledge of beam energy **Puts unprecedented demands on the tracker resolution** Δ(1/p_T) ~ 2 to 3 x10⁻⁵ (GeV/c)⁻¹ more than 10 times better than at LEP!



 μ^+ μ^- recoil mass at \sqrt{s} = 500 GeV. M_{H} = 120 GeV, for two values of the tracker resolution.

TPC development for the ILD central tracker

TPC an ideal central tracker for physics at ILC

Low mass, minimal photon conversion

·High efficiency, high granularity continuous tracking,

•Excellent pattern recognition,

•Particle ID

 $\cdot \Delta(1/p_T) \sim 1 \times 10^{-4} (GeV^{-1}) (TPC alone)$

~ 3.10⁻⁵ (GeV⁻¹) (vertex + Si inner tracker + TPC)

TPC parameters:

•200 track points

• $\sigma(r, \phi) \leq 100 \ \mu m$ includes stiff 90° tracks ~ 2 m drift • $\sigma(z) \sim 1 \ mm$ • $\sigma_{2 \ track}(r, \phi) \sim 2 \ mm$ • $\sigma_{2 \ track}(z) \sim 5 \ mm$

Limits on achievable TPC resolution

•The physics limit of TPC resolution comes from transverse diffusion: $\sigma_x^2 \approx \frac{D_{Tr}^2 \cdot z}{V_{eff}} N_{eff}$ = effective electron statistics.

•For best resolution, choose a gas with smallest diffusion in a high B field



Micro-Pattern Gas Detector R&D for ILD TPC

2 mm x 6 mm pads (1,500,000 channels) with GEMs or Micromegas proposed initially (TESLA TDR)
For the GEM, large transverse diffusion in the transfer & induction gaps provides a natural mechanism to disperse the charge improving centroid determination with wide pads.
LC TPC R&D: 2 mm pads too wide with conventional readout. The GEM TPC readout will need ~ 1 mm wide pads to achieve the 100 µm ILC resolution goal (~3,000,000 channels)
Even narrower pads needed for the Micromegas

Charge dispersion - a mechanism to disperse the MPGD avalanche charge so that wide pads can be used for centroid determination.

Charge dispersion in a MPGD with a resistive anode

•Modified GEM anode with a high resistivity film bonded to a readout plane with an insulating spacer.

•2-dimensional continuous RC network defined by material properties & geometry.

•Point charge at r = 0 & t = 0 disperses with time.

•Time dependent anode charge density sampled by readout pads. Equation for surface charge density function on the 2-dim. continuous RC network:

$$\frac{\partial \rho}{\partial t} = \frac{1}{RC} \left[\frac{\partial^2 \rho}{\partial r^2} + \frac{1}{r} \frac{\partial \rho}{\partial r} \right]$$
$$\Rightarrow \rho(r,t) = \frac{RC}{2t} e^{\frac{-r^2 RC}{4t}}$$



<u>Cosmic ray tests at DESY in a 5 Tesla magnet</u> <u>Micromegas TPC with charge dispersion readout</u>





 $\sim 50~\mu\text{m}$ av. resolution over 15 cm (diffusion negligible) 100 μm over 2 meters looks within reach!



<u>TPC Large Prototype (LP) Beam Test at DESY</u> by LC TPC Collaboration using EUDET Facility

<u>Goals</u>

- Study, in practice, design and fabrication of all components of MPGD TPC in larger scale; field cage, endplate, detector modules, front-end electronics and field mapping of non uniform magnetic field. (But not yet the engineering stage.)
- Demonstrate full-volume tracking in non-uniform magnetic field, trying to provide a proof for the momentum resolution at LC TPC.
- Demonstrate dE/dX capability of MPGD TPC.
- Study effects of detector boundaries.
- Develop methods and software for alignment, calibration, and corrections.



Design Study of the Magnetmovementtable



(Beijing tracker review, Jan 2007)

From Takeshi MATSUDA - 2nd RD51 Collaboration Meeting Paris 13 Oct, 2008

GEMs & Micromegas both being developed presently

GEMs readout with ~ 1 mm wide pads Micromegas with 2-3 mm wide pads charge dispersion readout





TPC endplate: 7 modules with Micromegas with charge dispersion readout.

To be built by Canada and France Large prototype in the 1 T magnet PCMAG. The 6 GeV electron beam will enter through the magnet coil transverse to the drift direction. The magnet has no iron.

Micromegas panel designed & fabricated at Saclay



<u>R&D specific to LP Micromegas panels</u>

- •Point to point variations of surface resistivity (R) and capacitance density (C) of anode pad readout structure must be minimized
- •Non-uniform RC response leads to systematic bias in position determination
- •Bias easy to correct for small 10 cm x 10 cm Micromegas tested so far
- •Development of Bulk Micromegas with resistive anode readout
- •AFTER front-end based on T2K readout electronics



Correction will be cumbersome for the larger area LP panels

Development of uniform high surface resistivity anode films

- Several techniques are being tested for the resistive anode coating
- Carbon-loaded Kapton. An old technique first tested at Carleton applied to bulk Micromegas with improvement in laminating resistive film to pad readout PCB
- First results promising. One panel produced.



2) Prepreg+ screen printing

Tried initially at CERN. Two prototypes of 10 cm x10 cm (2 and 8 MOhm/sq) have been tried at Saclay. Not clear if that they sparks are damped. One detector damaged by sparking Still such a layer will be applied to a CERN panel.



The panels

PCBs have been produced

4 with the Saclay routing in 6 layers

4 with the CERN routing with 4 layers





Two panels ready and tested at DESY



One with standard pads, one with resistive anode (C-loaded Kapton) Two more panels under construction, one with screen printing resistive anode, one with deposited layer

Mechanical support of electronics

Shielding, Faraday cage, flat cables, gas box...







LCWS 2008 Chicago

Tests at Saclay with a ⁵⁵Fe source



In-situ cosmic ray tests in test box at DESY









Data taken at 50 and 100 MHz, with shaping times of 200 ns, 400 ns, 1 & 2 µs

Event display



Presently developing software and analysis tools (D. Attié, S. Turnbull, Yun-Ha Shin, with Martin Killenberg): LCIO converter, JTPC, Marlin LCWS 2008 Chicago

AFTER electronics installation





Simulation for the LP keystone pads - New C++ program



<u>High momentum simulated track signal B = 4 T (keystone pads)</u>

 $Ar/CF_4/C_4H_{10}$ 95/3/2, E = 200 V/cm, v_{drift} = 73 µm/ns D_T = 23 µm/ \sqrt{cm} D_L =249 µm/ \sqrt{cm}



Micromegas risetime 50 ns, preamp rise time 40 ns, preamp decay time 2 μ s, Anode resistivity 1 M Ω/\Box , Dielectric gap = 75 μ m, dielectric constant 1.85 LCWS 2008 Chicago 26

Present status and plans

•TPC field cage tested to 19 kV in air and is presently being flushed with gas

- Magnet is ready
- •Move TPC to beam area
- Initial data taking with standard readout
 - •Cosmic rays
 - •With beam
 - •With beam and magnet
 - •Switch to resistive anode readout

Future plans

Start R&D for electronics on a mezzanine PCB. Planned for early 2010.

- R&D to optimize protection, compactness
- Development to test AFTER chips at the wafer level
- new card design

Make 7 fully equipped modules

Start cooling and integration studies